

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

( Not for submission under 37 CFR 1.99)

|                        |                     |
|------------------------|---------------------|
| Application Number     | 10001477            |
| Filing Date            | 2001-11-01          |
| First Named Inventor   | Steve Roe           |
| Art Unit               | 2123                |
| Examiner Name          | Jason Scott Proctor |
| Attorney Docket Number | CD01203M            |

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| 33 | M. Morris Mano, "Computer System Architecture," 1982, Prentice-Hall, 2nd Edition, pp. 261-264 and 435-440; 14 pages   | <input type="checkbox"/> |
| 34 | Dirk Killat, "A One-Chip Solution for Electronic Ballasts in Fluorescent Lamps," Power Electronics, < <a href="http://powerelectronics.com/mag/power_onechip_solution_electronic/">http://powerelectronics.com/mag/power_onechip_solution_electronic/</a> >, dated 03/01/2004, accessed 09/13/2005; 4 pages | <input type="checkbox"/> |
| 35 | Fred Eady, "PSoC 101," Circuit Cellar, August 2004, accessed 09/13/2005, < <a href="http://www.circuitcellar.com/library/print/0804/eady169/2.htm">http://www.circuitcellar.com/library/print/0804/eady169/2.htm</a> >; 4 pages   | <input type="checkbox"/> |
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| 41 | Application Number 12/058,569 (CD2007125): "Configuration of Programmable IC Design Elements," Best et al., filed on 03/28/2008; 19 pages                 | <input type="checkbox"/> |
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| 47 | John Mangino, "Using DMA with High Performance Peripherals to Maximize System Performance," 2007, Texas Instruments, pp. 1-23; 23 pages                   | <input type="checkbox"/> |
| 48 | Balough et al., "White Paper: Comparing IP Integration Approaches for FPGA Implementation," February 2007, Version 1.1, Altera, pp. 1-7; 7 pages          | <input type="checkbox"/> |
| 49 | A.F. Harvey, "DMA Fundamentals on Various PC Platforms," 2001, 2004, National Instruments Corporation, pp. 1-19; 19 pages                                 | <input type="checkbox"/> |
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| U.S. PATENTS      |         |               |                        |            |   |  |
|-------------------|---------|---------------|------------------------|------------|---|--|
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|                   | 1       | 5935233       | B1                     | 1999-08-10 | Jeddeloh, Joseph                                | Entire Document  |
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| 20 | 5497119 | B1 | 1996-03-05 | Tedrow et al.          | Entire Document |
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| 9  | USPTO Advisory Action for Application Number 11/818,005 (CD01171C) dated 07/30/2010; 3 pages     | <input type="checkbox"/> |
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